

Lesson Plan

Name of Faculty : Mr. Amit Kaushik
 Discipline : Electrical Engineering
 Semester : 6th
 Subject : Computer Aided Electrical Machine design
 Lesson Plan Duration : 11 week (January 2018 to April 2018)

Work load (Lecture/Practical) per week (in hours): Lecture-03, Tutorial-03, Practical-02

Week	Theory		Practical	
	Lecture Day	Topic	Practical Day	Topic
1	1 st	General features and limitations of electrical machine design	1	Study of design factors and its limitations-for gp-1
	2 nd	Types of enclosures		Study of design factors and its limitations-for gp-2
	3 rd	Heat dissipation		Study of design factors and its limitations-for gp-3
2	1 st	Temperature rise heating and cooling cycles and ratings of machine machines	2	Different approaches in CAD and its flow chart (i) Analysis method (ii) Synthesis method for gp-1
	2 nd	Cooling media used.		Different approaches in CAD and its flow chart (i) Analysis method (ii) Synthesis method for gp-2
	3 rd	Output equation and output coefficient		Different approaches in CAD and its flow chart (i) Analysis method (ii) Synthesis method for gp-3
3	1 st	Specific electric and magnetic loading	3	Core design of transformer for gp-1
	2 nd	Effect of size and ventilation		Core design of transformer for gp-2
	3 rd	MMF calculation for airgun and iron parts of electrical machines		Core design of transformer for gp-3
4	1 st	Gap contraction coefficient.	4	Length of air gap induction machine for gp-1

	2 nd	Real and apparent flux densities		Length of air gap induction machine for gp-2
	3 rd	Estimation of magnet current of transformers and rotating machines		Length of air gap induction machine for gp-3
5	1 st	Load current of transformers and induction motors	5	Calculation for losses of transformer for gp-1
	2 nd	Leakage flux reactance calculations for transformers rotating machines,		Calculation for losses of transformer for gp-2
	3 rd	Design of field magnet		Calculation for losses of transformer for gp-3
6	1 st	Design of transformer	6	Efficiency of transformer for gp-1
	2 nd	D.C. machines		Efficiency of transformer for gp-2
	3 rd	Induction motor		Efficiency of transformer for gp-3
7	1 st	Synchronous machine	7	Main design of Induction motor for gp-1
	2 nd	Performance calculations		Main design of Induction motor for gp-2
	3 rd	Problems		Main design of Induction motor for gp-3
8	1 st	Design on t/f	8	Design of Main dimension of synchronous motor for gp-1
	2 nd	Design Problems		Design of Main dimension of synchronous motor for gp-2
	3 rd	Specific Electrical and magnetic loading for dc machine		Design of Main dimension of synchronous motor for gp-3
9	1 st	Armature design	9	Design of slot dimension of synchronous machine for gp-1
	2 nd	Design of field winding		Design of slot dimension of synchronous machine for gp-2
	3 rd	Problem on armature and field design		Design of slot dimension of synchronous machine for gp-3

10	1 st	Computerization of design Procedures	10	Design the rotor slot for synchronous machine for gp-1
	2 nd	Development of Computer program		Design the rotor slot for synchronous machine for gp-2
	3 rd	Performance prediction		Design the rotor slot for synchronous machine for gp-3
11	1 st	Optimization techniques	11	Length of air gap synchronous machine for gp-1
	2 nd	Applications		Length of air gap synchronous machine for gp-2
	3 rd	Design Problems		Length of air gap synchronous machine for gp-3

LESSON PLAN

Name of Faculty: Mr. Sandeep Singh
 Discipline: Electrical Engineering
 Semester: 6th
 Subject: Control System Engineering
 Lesson Plan Duration: 11 weeks (Jan 2018 to April 2018)

Work Load (Lecture / Practical) per week (in hours) Lectures-03, Tutorial – 01, Pracicals-02

Week	Theory		Practical	
	Lecture Day	Topic (including assignment / test)	Practical Day	Topic
1 st	1 st	System/Plant model, types of models, illustrative examples of plants and their inputs and outputs.	1 st	Introduction of Lab Equipments and Discussion about Safety measures
	2 nd	Controller servomechanism, regulating system, linear time-invariant (LTI) system, time-varying system, causal system	2 nd	_____do_____
	3 rd	Open loop control system, closed loop control system, illustrative examples of open-loop and feedback control systems, continuous time and sampled data control systems.	3 rd	_____do_____
2 nd	1 st	Effects of feedback on sensitivity (to parameter variations), stability, external disturbance (noise), overall gain etc.	1 st	To study speed Torque characteristics of a) A.C. servo motor b) DC servo motor
	2 nd	Introductory remarks about non-linear control systems.	2 nd	_____do_____
	3 rd	Concept of transfer function, relationship between transfer function and impulse response, order of a system	3 rd	_____do_____
3 rd	1 st	Block diagram Algebra	1 st	a) To demonstrate simple motor driven closed loop DC position control system.

				(b) To study and demonstrate simple closed loop speed control system.
	2 nd	Numerical Problems on Block Diagram Algebra	2 nd	_____do_____
	3 rd	Test on Block Diagram Algebra	3 rd	_____do_____
4 th	1 st	Characteristic equation, derivation of transfer functions of electrical and electromechanical systems. Numerical Problems	1 st	To implement a PID controller for temperature control of a pilot plant.
	2 nd	Transfer functions of cascaded and non-loading cascaded elements and Introduction to state variable analysis and design	2 nd	_____do_____
	3 rd	Typical test signals, time response of first order systems to various standard inputs	3 rd	_____do_____
5 th	1 st	Discussion of Assignment on Time response of 2nd order system to step input, relationship between location of roots of characteristics equation, ω and ω_n , time domain specifications of a general and an under-damped 2nd order system, Numerical Problems	1 st	To study behavior of 1 order, 2 order type 0, type 1 system.
	2 nd	Steady state error and error constants	2 nd	_____do_____
	3 rd	Dominant closed loop poles, concept of stability, pole zero configuration and stability.	3 rd	_____do_____
6 th	1 st	Necessary and sufficient conditions for stability Hurwitz stability criterion Routh stability criterion and relative stability, Numerical Problems	1 st	To study control action of light control device.
	2 nd	Root locus concept, development of root loci for various systems, stability considerations.	2 nd	_____do_____
	3 rd	Numerical Problems on Root Locus	3 rd	_____do_____
7 th	1 st	Relationship between frequency response and time-response for 2nd order system	1 st	Different Toolboxes in MATLAB, Introduction to Control SystemsToolbox.
	2 nd	Polar Plots	2 nd	_____do_____
	3 rd	Numerical Problems on Polar Plots	3 rd	_____do_____

8 th	1 st	Nyquist Criterion	1 st	Plot the pole-zero configuration in s-plane for the given transfer function.
	2 nd	Numerical Problems on Nyquist Criterion	2 nd	_____do_____
	3 rd	Bode Plots	3 rd	_____do_____
9 th	1 st	Numerical Problems on Bode Plots	1 st	Plot root locus of given transfer function and to find out S, Wd, Wn at given root & to discuss stability.
	2 nd	Stability, Gain-margin and Phase Margin, relative stability, frequency response specifications	2 nd	_____do_____
	3 rd	Necessity of compensation, compensation networks	3 rd	_____do_____
10 th	1 st	Application of lag and lead compensation	1 st	Plot the Nyquist plot for given transfer function and to discuss closed loop stability, gain and phase margin.
	2 nd	Numerical Problems on Compensation	2 nd	_____do_____
	3 rd	Basic modes of feedback control, proportional, integral and derivative controllers	3 rd	_____do_____
11 th	1 st	Illustrative Examples on PID	1 st	Plot bode plot of given transfer function and find gain and hase margins
	2 nd	Discussion of Assignment on Synchronos	2 nd	_____do_____
	3 rd	Class Test	3 rd	_____do_____
12 th	1 st	AC and DC Techo-generators	1 st	Practical Problems
	2 nd	Stepper Motors &Applications	2 nd	_____do_____
	3 rd	Magnetic Amplifier	3 rd	_____do_____

LESSON PLAN

Name of Faculty : Ms. Ritu Yadav
 Discipline : Electrical Engineering
 Semester : 6th
 Subject : ELECTRICAL POWER GENERATION
 Lesson Plan Duration : 11 week (January 2018 to April 2018)

Work load (Lecture/Practical) per week (in hours): Lecture-03, Tutorial-01

Week	Theory		Practical	
	Lecture Day	Topic	Practical Day	Topic
1	1 st	INTRODUCTION Energy sources	1	NA
	2 nd	Availability		NA
	3 rd	Recent trends in Power Generation		NA
2	1 st	Interconnected Generation of Power Plants	2	NA
	2 nd	POWER GENERATION PLANNING Load forecasting		NA
	3 rd	Load curves		NA
3	1 st	Load duration curve	3	NA
	2 nd	Base load and Peak load Power Plants		NA
	3 rd	Connected Load		NA
4	1 st	Maximum demand	4	
	2 nd	Demand factor		
	3 rd	Group diversity factor		
5	1 st	Load factor	5	NA
	2 nd	Significance of load factor		NA
	3 rd	Plant factor		NA
6	1 st	Capacity factor	6	NA
	2 nd	Selection of unit size, No. of Units		NA

	3 rd	Reserves, cost of power generation		NA
7	1 st	Depreciation, tariff	7	NA
	2 nd	Selection of site		NA
	3 rd	Capacity calculations		NA
8	1 st	Classification, Schematic diagram and working of Thermal Power Stations	8	NA
	2 nd	Hydro Electric Plant		NA
	3 rd	Nuclear Power Plant		NA
9	1 st	Diesel Power Stations	9	NA
	2 nd	NON-CONVENTIONAL ENERGY SOURCES		NA
	3 rd	Wind, Solar, Tidal, Ocean		NA
10	1 st	Geothermal sources of Energy	10	NA
	2 nd	Fuel cell		NA
	3 rd	Magneto Hydro Dynamic (MHD) system		NA
11	1 st	Energy management	11	NA
	2 nd	Energy Audit		NA
	3 rd	Energy Efficient Motors, Co-generation		NA

Lesson Plan

Name of Faculty : Mr. Amit Kaushik
 Discipline : Electrical Engineering
 Semester : 6thSem
 Subject : Power System - II
 Lesson Plan Duration : 11 week (January 2018 to April 2018)

Work load (Lecture/Practical) per week (in hours): Lecture-03, Tutorial-01, Lab - 02

Week	Theory	
	Lect. Day	Topic(Including assignment/test)
1	1	Introduction to faults and fault analysis in power system.
	2	Transients on a transmission line. Transient analysis on a transmission line.
	3	Short circuit analysis of synchronous machine at no load and on full load.
2	1	Symmetrical component of power system.
	2	Symmetrical component transformation. Phase shift in star-delta transformation.
	3	Sequence impedances.
3	1	Single line to ground fault, Line to line fault.
	2	Double line to ground fault, Open conductor fault.
	3	Circuit breakers, circuit breaker ratings
4	1	Theory of arc interruption, Re-striking voltage transients
	2	Current chopping,
	3	Automatic switch, duties of switch gear.
5	1	Air circuit breaker, air blast,
	2	Bulk oil, minimum oil,
	3	SF6 CB, vacuum and DC circuit breakers.
6	1	Protection of Transformer,
	2	Protection of generator
	3	Protection of motor and bus zone.
7	1	Protective Relays: zone of protection, Essential qualities,
	2	Primary and backup protections,
	3	Relay classification, Principal types of electromagnetic relays, attracted armature, induction disc, induction cup types.
8	1	Over -current, instantaneous over current relay.

	2	IDMT, directional and differential relays,
	3	Distance relays, plain impedance,
9	1	MHO, Reactance, Offset MHO type,
	2	Transmission line & feeder protection
	3	Pilot wire and carrier current protection, neutral grounding.
10	1	Classification of static relays, amplitude and phase comparators,
	2	Block-spike and block-average comparators,
	3	Rectifier type relays.
11	1	Introduction to digital relay: basic principles. Travelling wave relay,
	2	Application of microprocessors and computers - recent Trends.
	3	Relaying schemes based on microwave and optical fiber link.

Practical	
Practical day	EXPERIMENT
1	To draw the operating characteristics of IDMT relay.
2	To study the performance of Earth fault relay.
3	To study the performance of a over voltage relay.
4	To study the performance of under voltage relay.
5	Testing of breakdown strength of transformer oil.
6	To study flash point test of transformer oil.
7	To find ABCD ,Hybrid & Image parameters of a model of transmission line
8	To study performance of a transmission line under no load condition & under load at different power factors.
9	To study performance characteristics of typical DC distribution system in radial & ring main configuration
10	To study radial feeder performance when a) fed at one end b) fed at both ends.

LESSON PLAN

Name of Faculty: SAWAN
 Discipline: Electronics & Communication Engineering
 Semester: 6th
 Subject: Microcontroller and Embedded Systems (ECE+EE)
 Lesson Plan Duration: 15 weeks (Jan 2018 to April 2018)

Work Load (Lecture / Practical) per week (in hours) Lectures-03, Pracicals-06

Week	Theory		Practical	
	Lecture Day	Topic (including assignment / test)	Practical Day	Topic
1 st	1 st	Difference between microprocessors and microcontrollers and different types of microcontrollers	1 st	Write an Assembly language Programme (ALP) to generate 10 kHz square wave.
	2 nd	Embedded microcontrollers, External memory microcontrollers	2 nd	-----do-----
	3 rd	Processor Architectures Harvard V/S Princeton	3 rd	-----do-----
2 nd	1 st	CISC V/S RISC	1 st	To study implementation & interfacing of Display devices Like LCD, LED Bar graph & seven segment display with Microcontroller 8051/AT89C51
	2 nd	Microcontrollers features : clocking	2 nd	-----do-----
	3 rd	i/o pins, interrupts	3 rd	-----do-----
3 rd	1 st	Continue..	1 st	Revision
	2 nd	Discussion on Assignment.	2 nd	-----do-----
	3 rd	Class Test	3 rd	-----do-----
4 th	1 st	Microcontroller 8051- Architecture	1 st	Write an ALP for temperature & pressure measurement.
	2 nd	Pin Diagram of 8051	2 nd	-----do-----
	3 rd	I/O Ports of 8051	3 rd	-----do-----

5 th	1 st	InternalRAM and Registers, Memory Organization and external addressing	1 st	Write a program to interface a graphical LCD with 89C51.
	2 nd	Interrupts, Addressing Modes of 8051	2 nd	-----do-----
	3 rd	Instruction Set of 8051	3 rd	-----do-----
6 th	1 st	Instruction Set of 8051 cntd..	1 st	Revision 1
	2 nd	Assembly Language Programming Examples	2 nd	-----do-----
	3 rd	Assembly Language Programming Examples cntd..	3 rd	-----do-----
7 th	1 st	Interfacing of LCD with 8051	1 st	To study Programming and Transmission & reception of data through Serial port & study of Parallel printer port.
	2 nd	Interfacing of ADC & DAC with 8051	2 nd	-----do-----
	3 rd	Interfacing of stepper motor with 8051	3 rd	-----do-----
8 th	1 st	Interfacing of Key board and sensors with 8051.	1 st	Repeat previous
	2 nd	Discussion on Assignment	2 nd	-----do-----
	3 rd	Introduction to PIC microcontrollers,	3 rd	-----do-----
9 th	1 st	Architecture and pipelining in PIC	1 st	To interface PWM based voltage regulator using PIC Microcontroller
	2 nd	Architecture and pipelining in PIC cntd..	2 nd	-----do-----
	3 rd	program memory considerations in PIC	3 rd	-----do-----
10 th	1 st	Addressing modes, CPU registers in PIC	1 st	Repeat previous
	2 nd	Instruction set of PIC	2 nd	-----do-----
	3 rd	Simple operations using PIC Instructions	3 rd	-----do-----
11 th	1 st	Simple operations using PIC Instructions	1 st	Revision 2
	2 nd	Class Test	2 nd	Revision 2
	3 rd	Embedded Systems-Introduction	3 rd	Revision 2
12 th	1 st	Detailed classification of embedded systems.	1 st	Study and analysis of interfacing of Graphical LCD using PIC controller

	2 nd	Different type of processors used in embedded systems	2 nd	-----do-----
	3 rd	Hardware Units, Software Embedded into System,	3 rd	-----do-----
13 th	1 st	Applications and Products of Embedded Systems	1 st	Study and interfacing of IR (RC5 protocol) and RF Communication using PIC controller
	2 nd	Structural Units in Processor,	2 nd	-----do-----
	3 rd	Memory Devices, I/O Devices	3 rd	-----do-----
14 th	1 st	Different types of Buses	1 st	Study of SD/MMC card Interface using 18F4550
	2 nd	Interfacing of Processor Memory and I/O Devices,	2 nd	-----do-----
	3 rd	Interfacing of Processor Memory and I/O Devices contd..	3 rd	-----do-----
15 th	1 st	Case Study of an Embedded System for a Smart Card.	1 st	Revision 2
	2 nd	Class Test	2 nd	Revision 2
	3 rd	Programming Problems	3 rd	Revision 2

Name of Faculty : NEHA GUPTA
 Discipline : ECE
 Semester : 6th Sem
 Subject : Digital System Design, Digital System Design Lab
 Lesson Plan Duration : 15 weeks (From Jan 2018 to April, 2018)

Work Load (Lecture / Practical) per week (in hours) Lectures-03, Pracicals-06

Week	Theory		Practical	
	Lecture Day	Topic (including assignment / test)	Practical Day	Topic
1 st	1 st	Introduction to Computer-aided design tools for digital systems, Hardware description languages	1 st	Familiar With EDA tool
	2 nd	Introduction to VHDL data objects	2 ndDo.....
	3 rd	Classes and data type	3 rdDo.....
2 nd	1 st	Classes and data type (Continues...)	1 st	Design all gates using VHDL.
	2 nd	Operators, Overloading	2 ndDo.....
	3 rd	Logical operators, Types of delays	3 rdDo.....
3 rd	1 st	Entity and Architecture declaration	1 st	Problem with experiments
	2 nd	Class Test	2 ndDo.....
	3 rd	Assignment statements, sequential statements and process	3 rdDo.....
4 th	1 st	Conditional statements, case statement, Array	1 st	Write VHDL programs for the following circuits, check the wave forms and the hardware generated a. half adder b. full adder
	2 nd	Loops, resolution functions	2 ndDo.....
	3 rd	Packages and Libraries	3 rdDo.....

5 th	1 st	Concurrent statements. Subprograms	1 st	Write VHDL programs for the following circuits, check the wave forms and the hardware generated a. multiplexer b. demultiplexer
	2 nd	Application of Functions and Procedures	2 ndDo.....
	3 rd	Structural Modeling, component declaration	3 rdDo.....
6 th	1 st	Structural layout and generics	1 st	Write VHDL programs for the following circuits, check the wave forms and the hardware generated a. decoder b. encoder
	2 nd	Discussion - Coding based Problem	2 ndDo.....
	3 rd	Discussion - Coding based Problem	3 rdDo.....
7 th	1 st	Multiplexers, Demultiplexers	1 st	Write a VHDL program for a comparator and check the wave forms and the hardware generated
	2 nd	Model of encoders, decoders	2 ndDo.....
	3 rd	Coding based Problem	3 rdDo.....
8 th	1 st	Coding based Problem	1 st	Write a VHDL program for a code converter and check the wave forms and the hardware generated
	2 nd	Model of code converters	2 ndDo.....
	3 rd	Coding based Problem	3 rdDo.....
9 th	1 st	Model of Comparators, implementation of Boolean functions etc.	1 st	Write a VHDL program for a FLIP-FLOP and check the wave forms and the hardware generated
	2 nd	Coding based Problem	2 ndDo.....

	3 rd	Model of Shift Registers	3 rdDo.....
10 th	1 st	Coding based Problem	1 st	Write a VHDL program for a counter and check the wave forms and the hardware generated
	2 nd	Model of Counters	2 ndDo.....
	3 rd	Coding based Problem	3 rdDo.....
11 th	1 st	Model of Counters	1 st	Write VHDL programs for the following circuits, check the wave forms and the hardware generated a. register
	2 nd	Coding based Problem	2 ndDo.....
	3 rd	Assignment --- Designing of a simple microcomputer system using VHDL , FPGA and CPLD	3 rdDo.....
12 th	1 st	Basic components of a computer, specifications	1 st	Problem with experiments
	2 nd	Architecture of a simple microcomputer system	2 ndDo.....
	3 rd	Implementation of a simple microcomputer system using VHDL	3 rdDo.....
13 th	1 st	Introduction of Programmable logic device	1 st	Write VHDL programs for the following circuits, check the wave forms and the hardware generated b. Shift register
	2 nd	ROM , PLAs	2 ndDo.....
	3 rd	PAL ,GAL and PEEL	3 rdDo.....
14 th	1 st	Designing Problem with ROM,PLA,PLA	1 st	Design a 3: 8 decoder
	2 nd	Designing Problem with ROM,PLA,PLA	2 ndDo.....

	3 rd	CPLDs and FPGA	3 rdDo.....
15 th	1 st	Design implementation using CPLDs and FPGAs	1 st	Internal Vice Voce
	2 nd	Design implementation using CPLDs and FPGAs (Continues...)	2 nd	Internal Vice Voce
	3 rd	Class Test	3 rd	Internal Vice Voce